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JC979 U.S. PTO
10/072346
02/06/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Appn.Ser. No. (Doc.No. YO998-423) : Art Unit Not Yet Assigned
Filed Herewith : Examiner Not Yet Assigned
Applicants Coteus et al : Atty. Dkt. YO998-423

INFORMATION DISCLOSURE TRANSMITTAL LETTER

Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Please find enclosed a listing (PTO Form 1449 type) of references with copies which may be relevant to the above identified application. This listing of references is not a representation that a search has been made, that better references than listed do not exist, or that other references are not applicable. Moreover, this listing does not constitute any admission by applicants or applicants' attorney that the information provided herein is necessarily prior art to applicants' invention.

Submitted also is a copy of the referenced provisional patent application Ser. No. 60/104,889
Filed 10/20/98.

Respectfully submitted,

Alvin J. Riddles 10/14/99
Alvin J. Riddles
Reg.No. 17862

Docket No. YO998-423

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

ATTY. DOCKET NO.

Y0998-423

SERIAL NO.

NOT YET ASSIGNED

APPLICANT

COTEUS ETAL

FILING DATE

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

JP979 U.S. PTO
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
													YES	N
	AL													
	AM													
	AN													
	AO													
	AP													

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	"256Mb DOUBLE DATA RATE SYNCHRONOUS DRAM", IBM BROCHURE, 33L8026 FK5424, 1/99, 67 PAGES
AS	"16Mx72 2 BANK REGISTERED DDR SDRAM MODULE", IBM BROCHURE, 19L204 FIVER, 11/98, PAGES 1-8
AT	

EXAMINER

DATE CONSIDERED